

U.S. Patent No.: 7,428,975  
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Examiner: Anthony D. Stashick  
Applicant: Judson A. Bradford  
Title: COMBINATION OF PARTITION ASSEMBLY  
AND CONTAINER AND METHOD OF USE  
Confirmation No.: 7525

Cincinnati, Ohio 45202

January 6, 2009

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**PRINTING ERRORS MADE OF RECORD**

The following errors were noted in the printing of the above-identified U.S. patent:

Column 2, line 7, "not be secured" should be - - - not secured - - -.

Column 3, line 39, "being secured the" should be - - - being secured to the - -.

Column 3, line 55, "16, 18 are" should be - - - 16, 18 is - - -.

Column 4, line 61, "visa versa" should be - - - vice versa - - -.

Column 4, line 66, "56" should be - - - 54 - - -.

Column 5, line 8, "hooks or loop" should be - - - hooks or loops - - -.

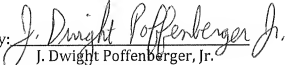
Column 5, line 59, "matrix." should be - - - matrix; - - -.

Column 8, line 3, "partitions wherein" should be - - - partitions, wherein - - -.

These errors do not appear to be of sufficient importance to warrant issuance of a Certificate of Correction. However, it is requested that this letter be placed in the file wrapper history to make these errors of record.

Respectfully submitted,

WOOD, HERRON & EVANS, LLP

By:   
J. Dwight Poffenberger, Jr.  
Reg. No. 35,324

2700 Carew Tower  
Cincinnati, Ohio 45202  
(513) 241-2324